

8

7

6

5

4

3

2

1

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REV A

DESCRIPTION INITIAL RELEASE

DATE 05NOV24

APPROVED V. SETHIA

REV B

DESCRIPTION AS PER ECR-125057

DATE 13MAR25

APPROVED J. GARCIA

REV B1

DESCRIPTION AS PER ECR-126287

DATE 11AUG25

APPROVED J. GARCIA

EVAL-AD5710R-ARDZ SCHEMATIC REV (B)

PAGE 2 - POWER CIRCUITRY

PAGE 3 - MAIN DEVICE CIRCUITRY

PAGE 4 - DIGITAL CONNECTIVITY

P.O SPEC.

BK/BD SPEC.

SOCKET OEM

OEM PART#

HANDLER

8

7

6

5

4

3


2

1

TEMPLATE ENGINEER
N/A

DATE
N/A

SCHEMATIC



HARDWARE SERVICES
M. VALE

05NOV24

HW TYPE : Customer Evaluation Z

HARDWARE SYSTEMS
N/A

N/A

Product(s): AD5710R

TEST ENGINEER
N/A

N/A

: AD5711R

COMPONENT ENGINEER
N. CATALAN

05NOV24

PACKAGE : 25-lead BodySize WLCSP-family

TEST PROCESS
N/A

N/A

: Pitch-pitch StyleVendor Style

HARDWARE RELEASE
E. CONDA

05NOV24

<User Define>

DESIGNER
C. SÁRIEGO

05NOV24

<User Define>

PTD ENGINEER
V. SETHIA

05NOV24

<User Define>

CHECKER
N/A

N/A

MASTER PROJECT TEMPLATE
ARDUINO_UNO_SHIELD_EV164489_d

TESTER TEMPLATE
N/A

DRAWING NO.
02_083459

REV.
b1

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES

TOLERANCES

DECIMALS
X.XX +0.010
X.XXX +0.005

FRACTIONS
+1/32

ANGLES
+2

SIZE
D

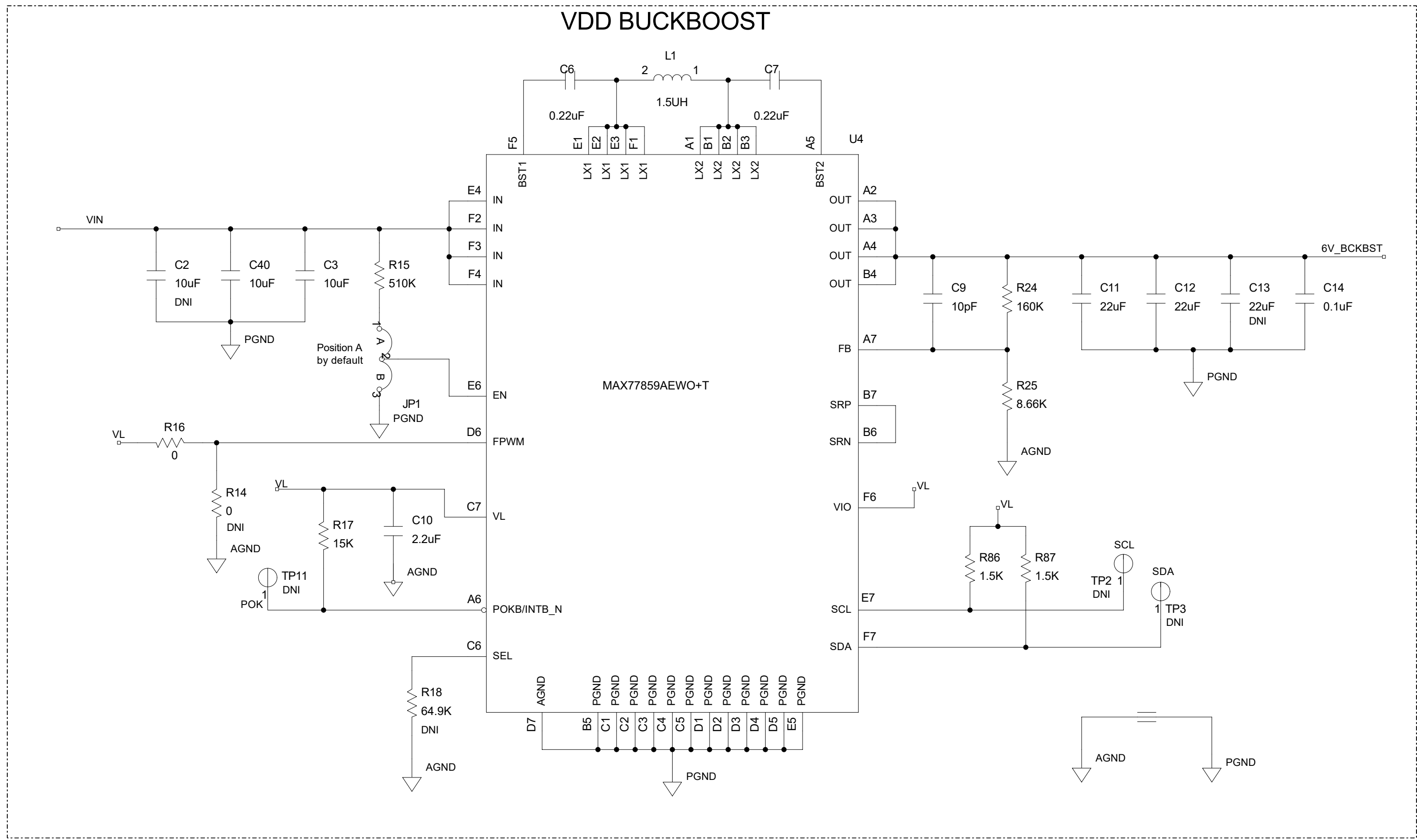
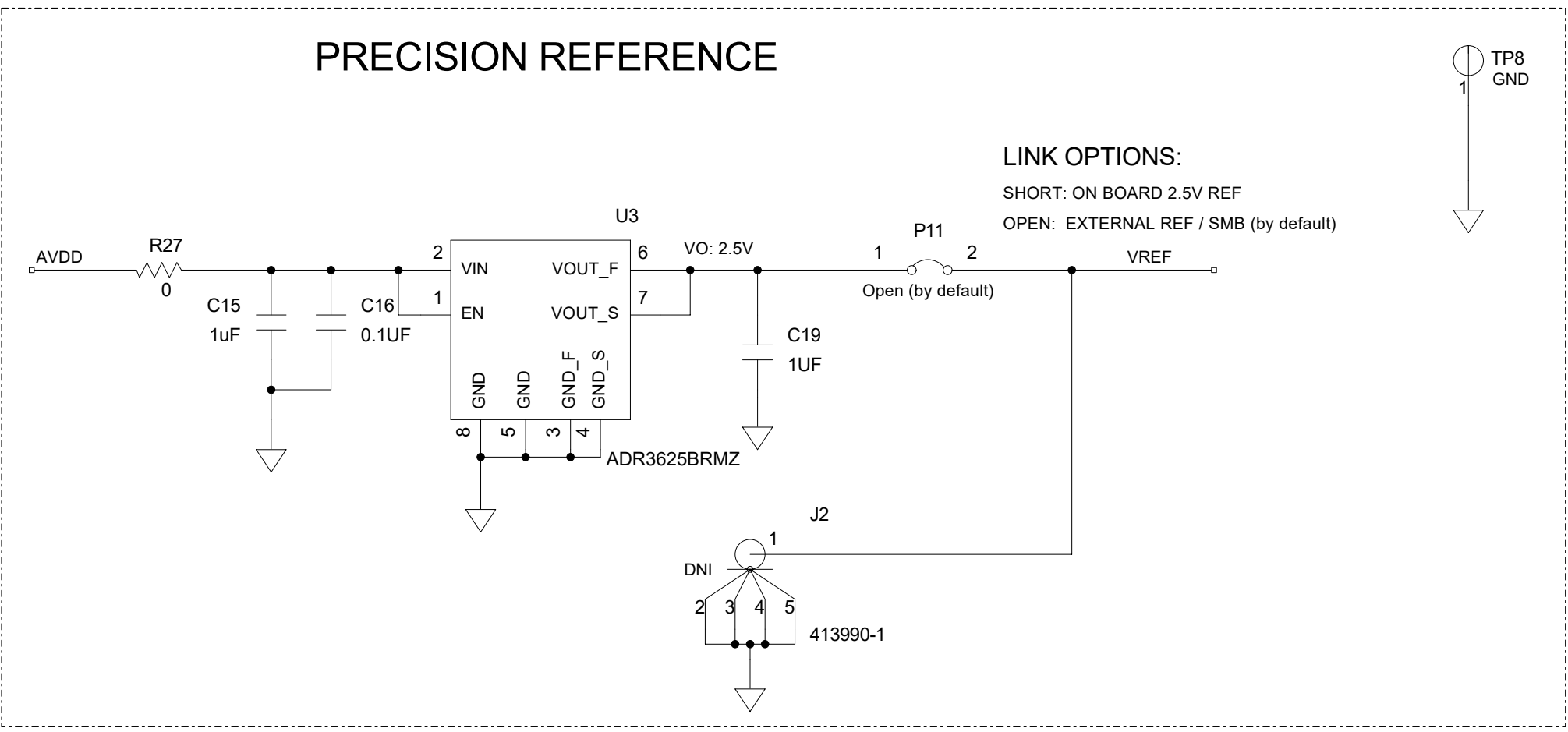
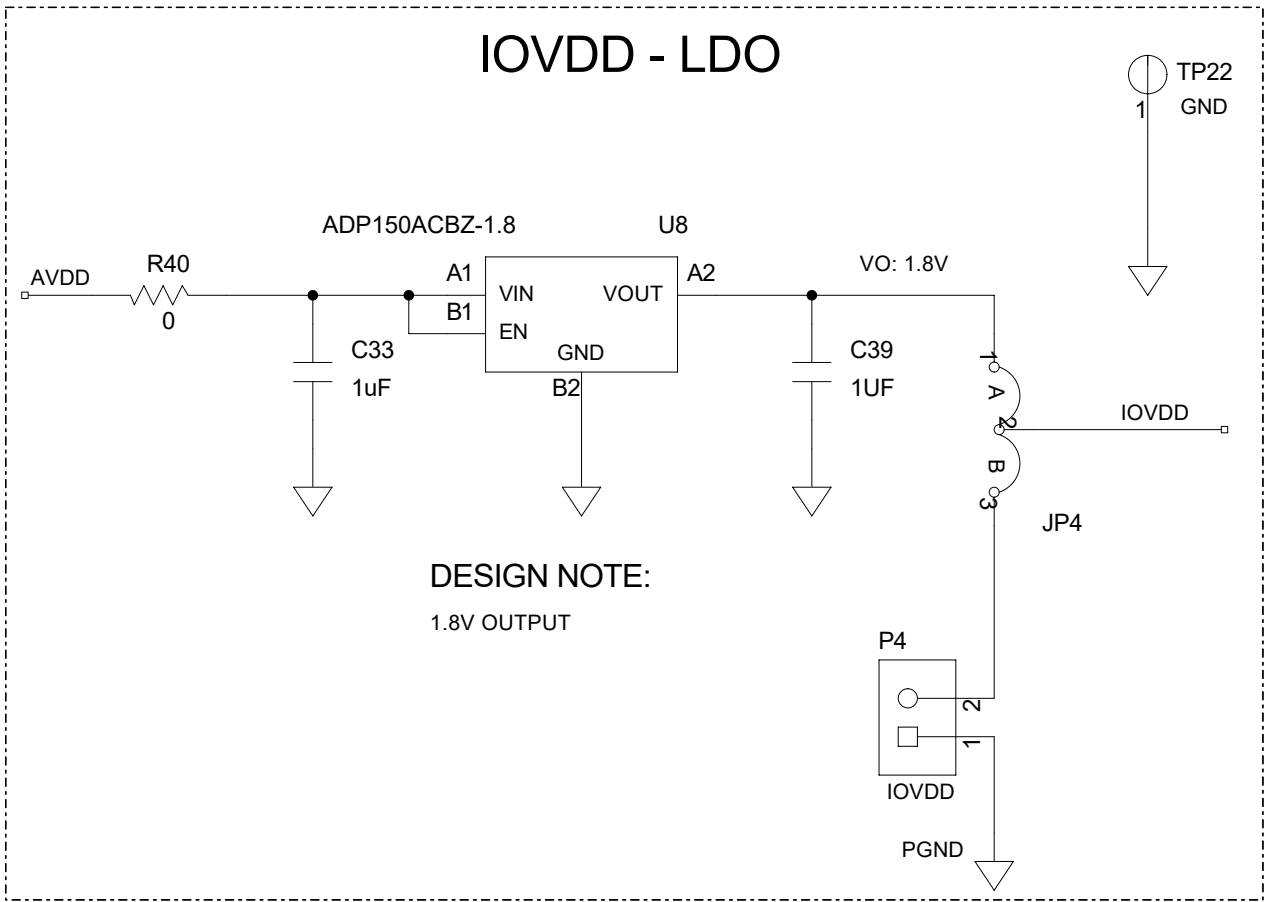
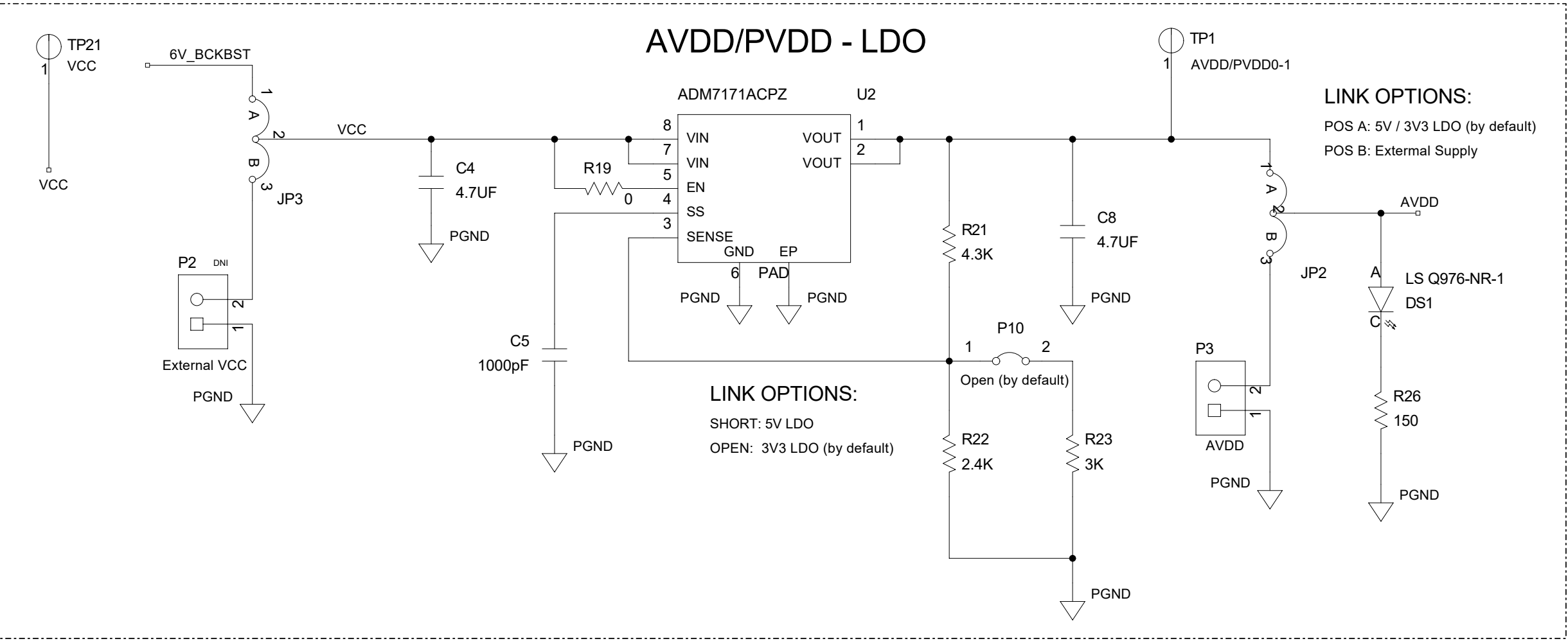
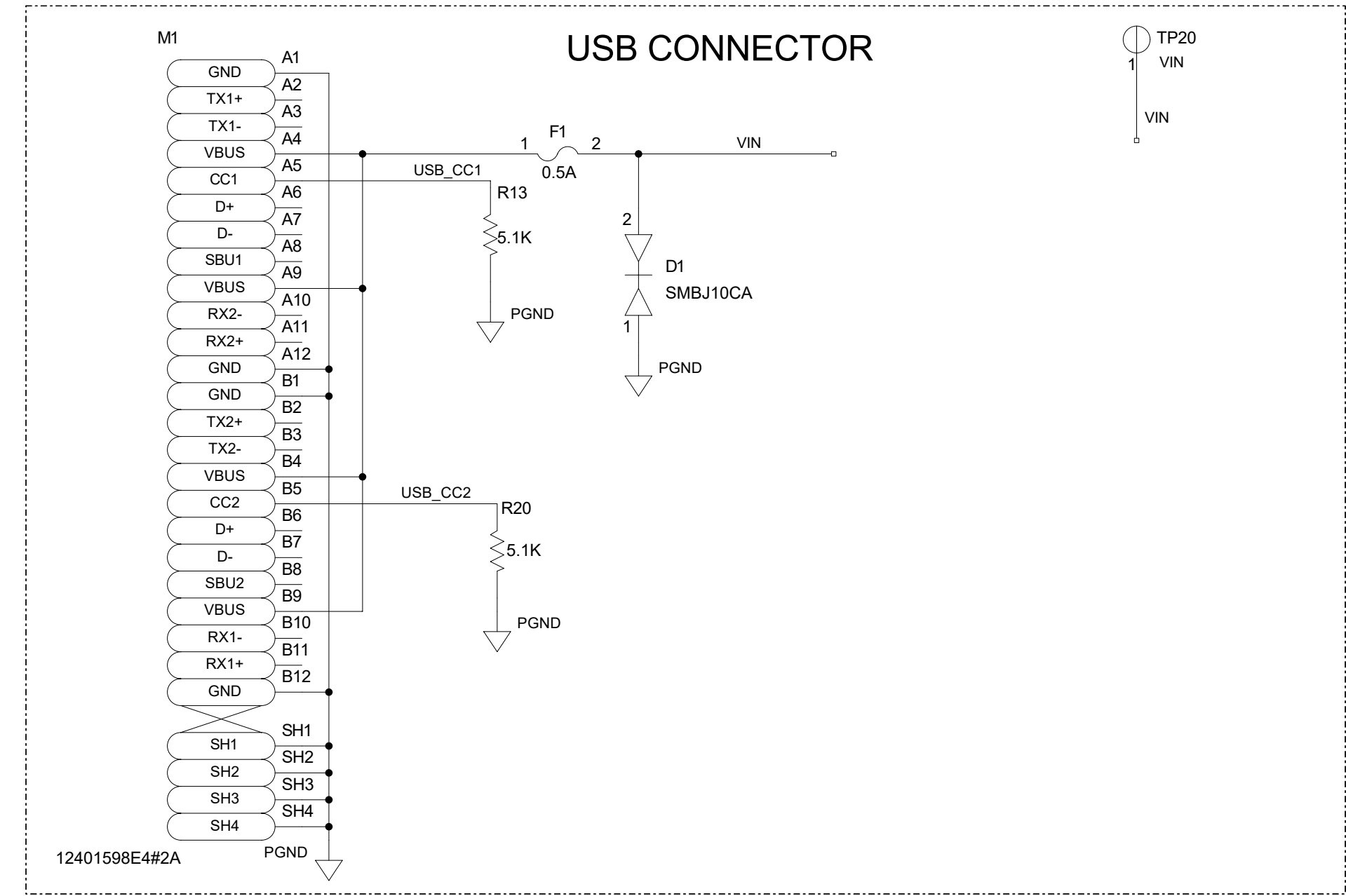
SCALE
1:1

CODE ID NO.
CodeID

SHEET 1 OF 4

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

POWER CIRCUITRY

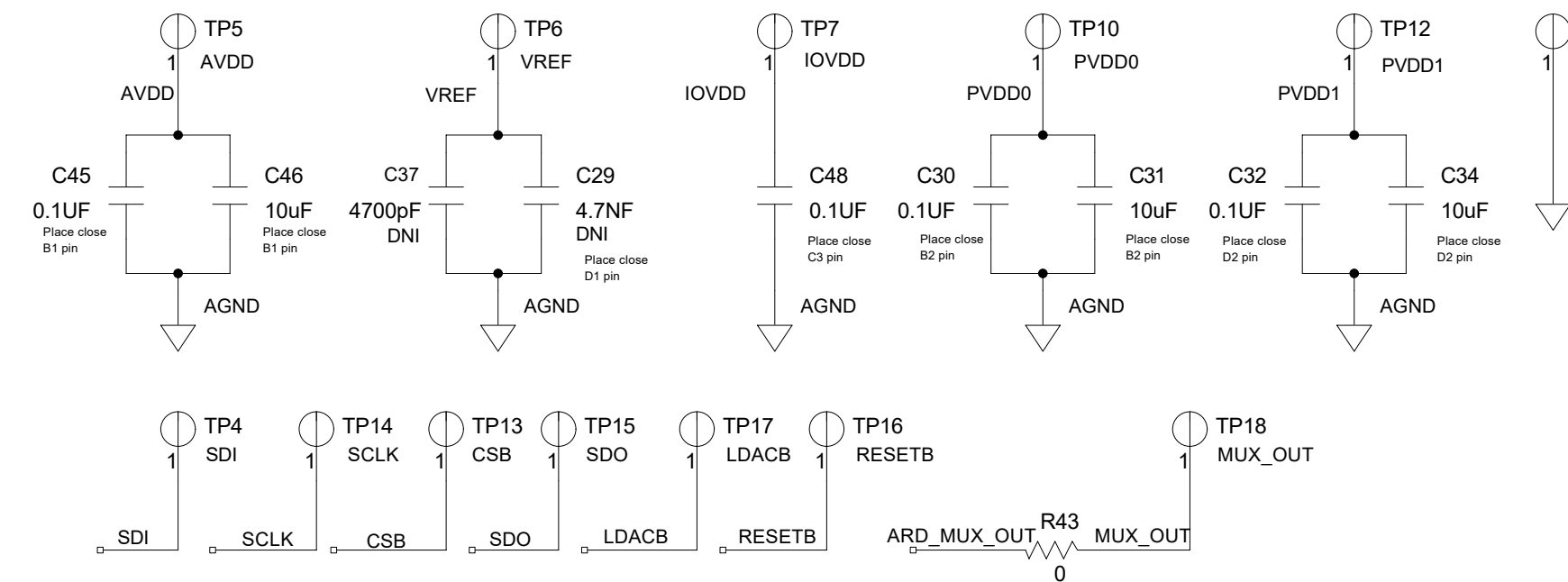
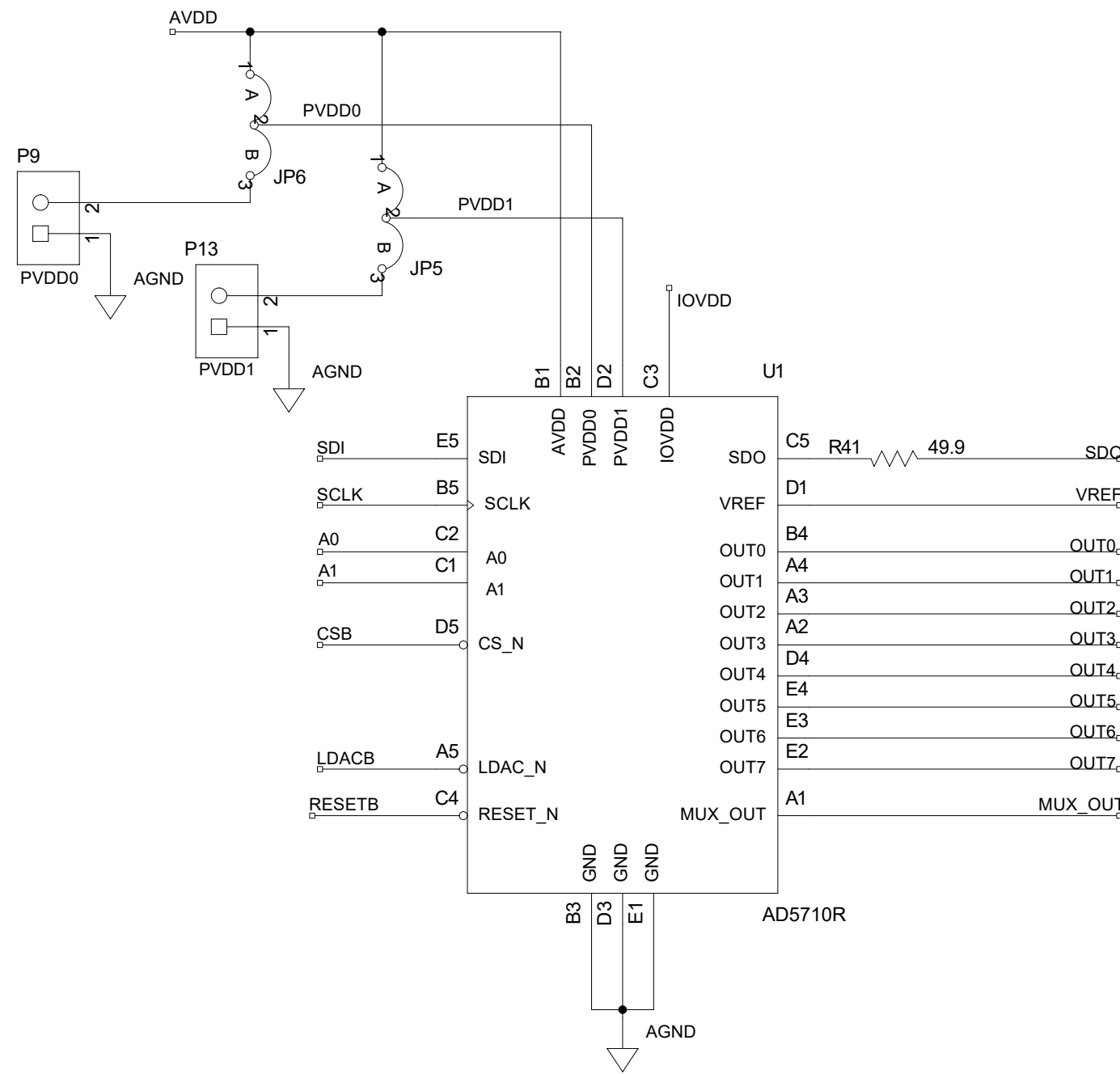
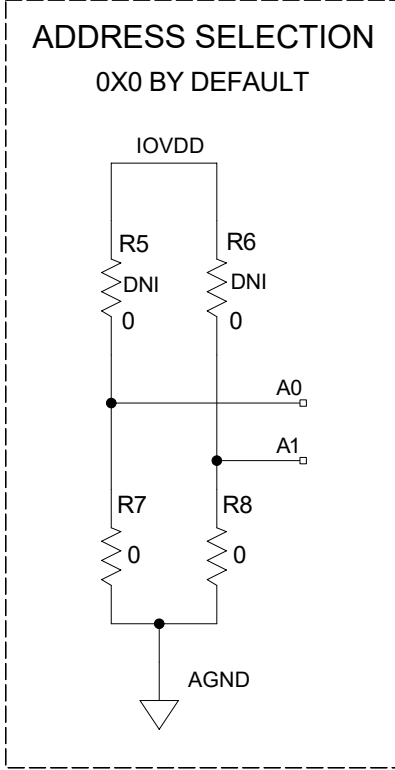


ANALOG DEVICES		SCHEMATIC			
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		DESIGN VIEW \$DESIGN_VIEW		REV b1	
		PTD ENGINEER V. SETHIA		SIZE D	SCALE 1:1
		SHEET 2 OF 4			

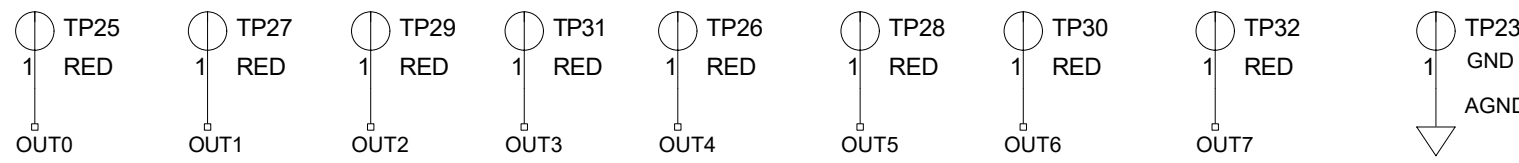
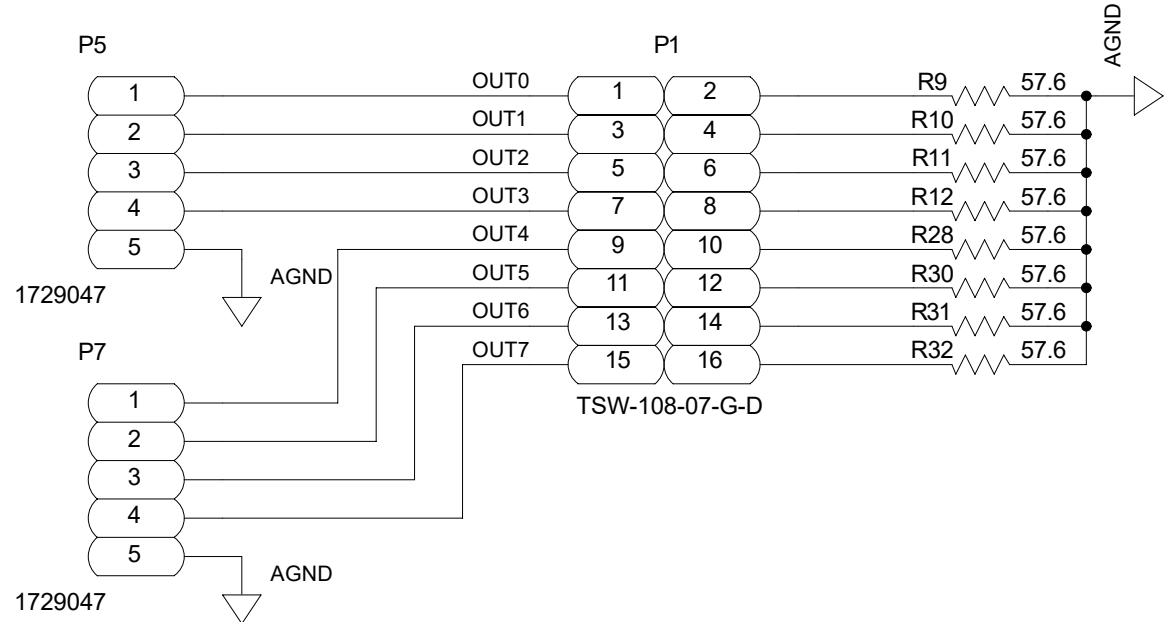
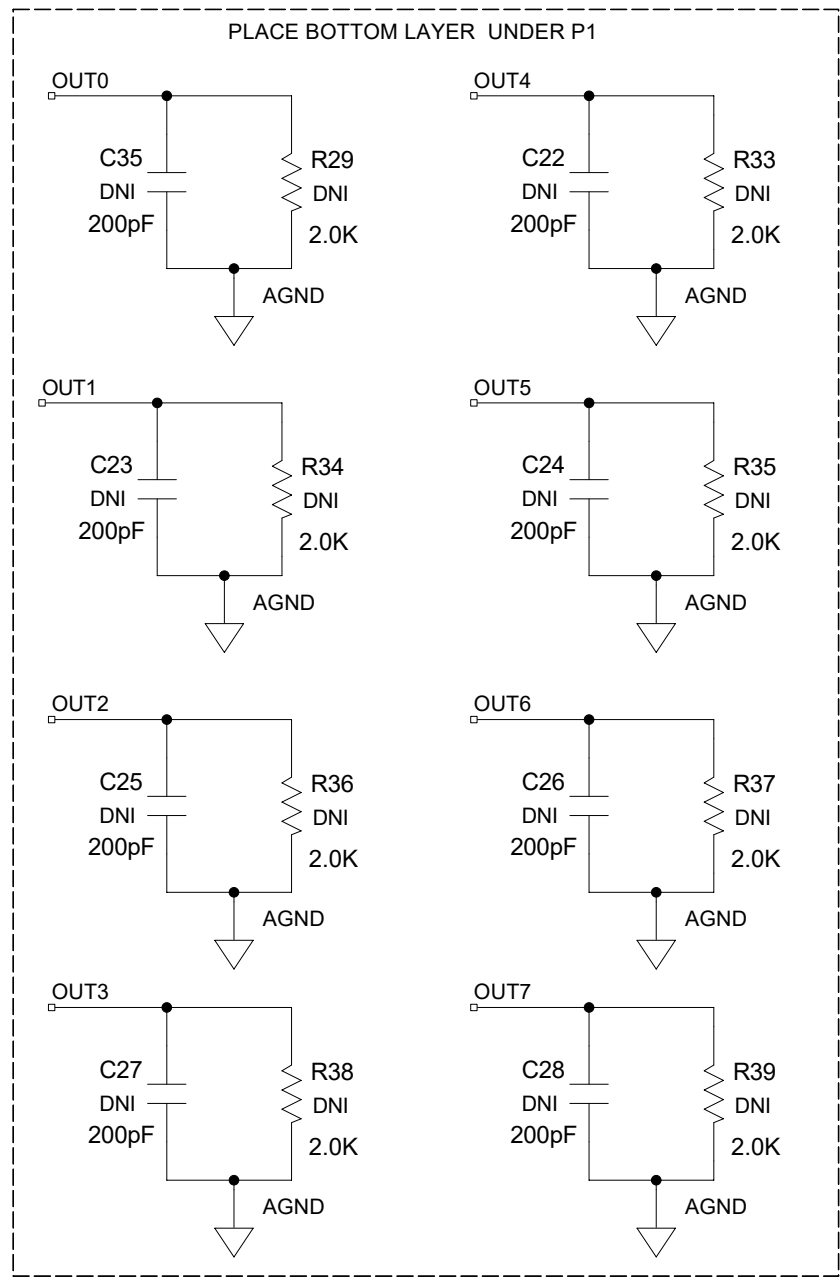
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

MAIN DEVICE CIRCUITRY

MAIN CIRCUIT



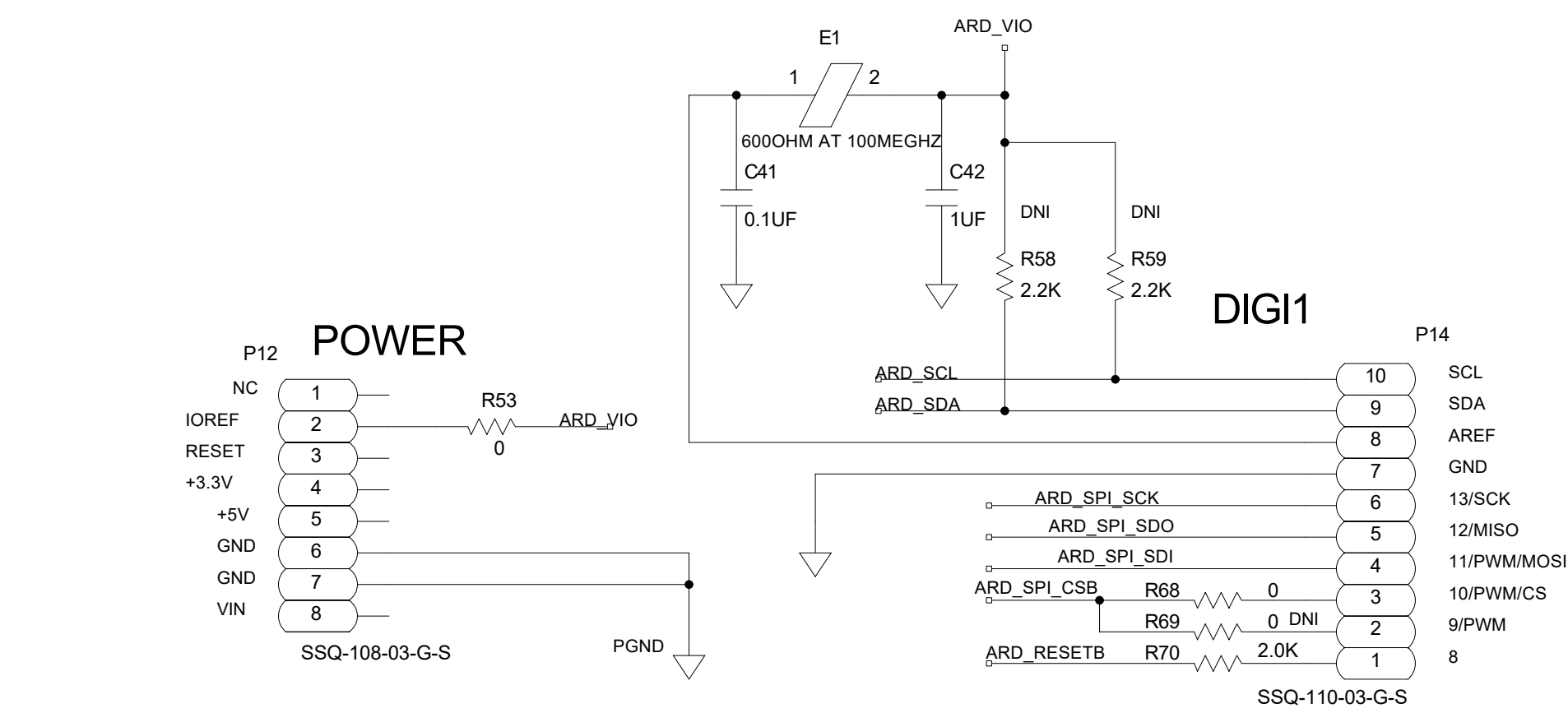
DAC/IDAC OUTPUTS



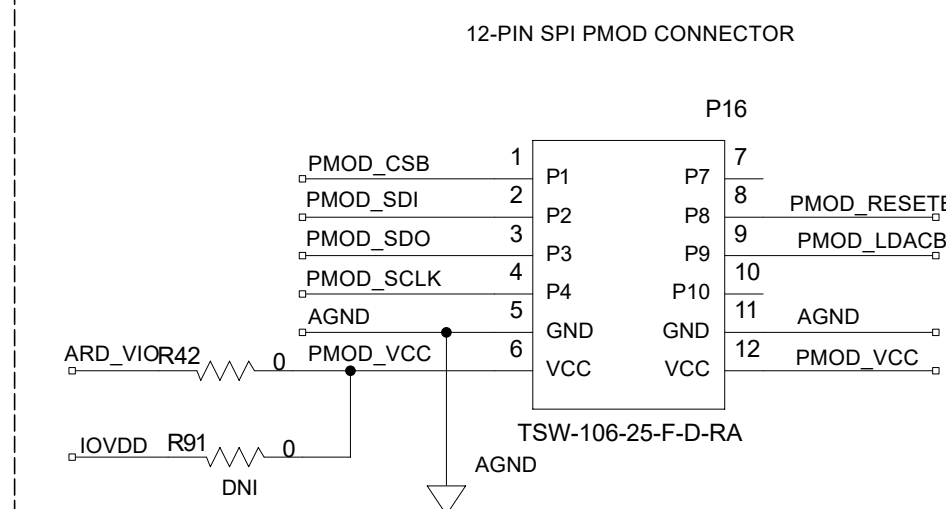
ANALOG DEVICES		SCHEMATIC			
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		DESIGN VIEW \$DESIGN_VIEW		DRAWING NO. 02_083459	
		PTD ENGINEER V. SETHIA		SIZE D	SCALE 1:1
		SHEET 3 OF 4		REV b1	

DIGITAL CONNECTIVITY

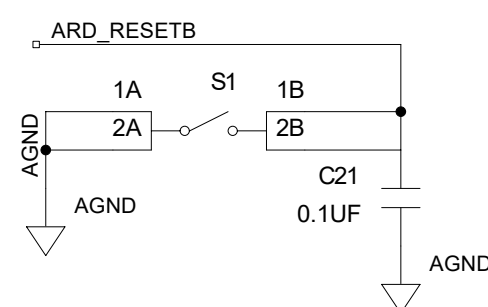
ARDUINO HEADERS



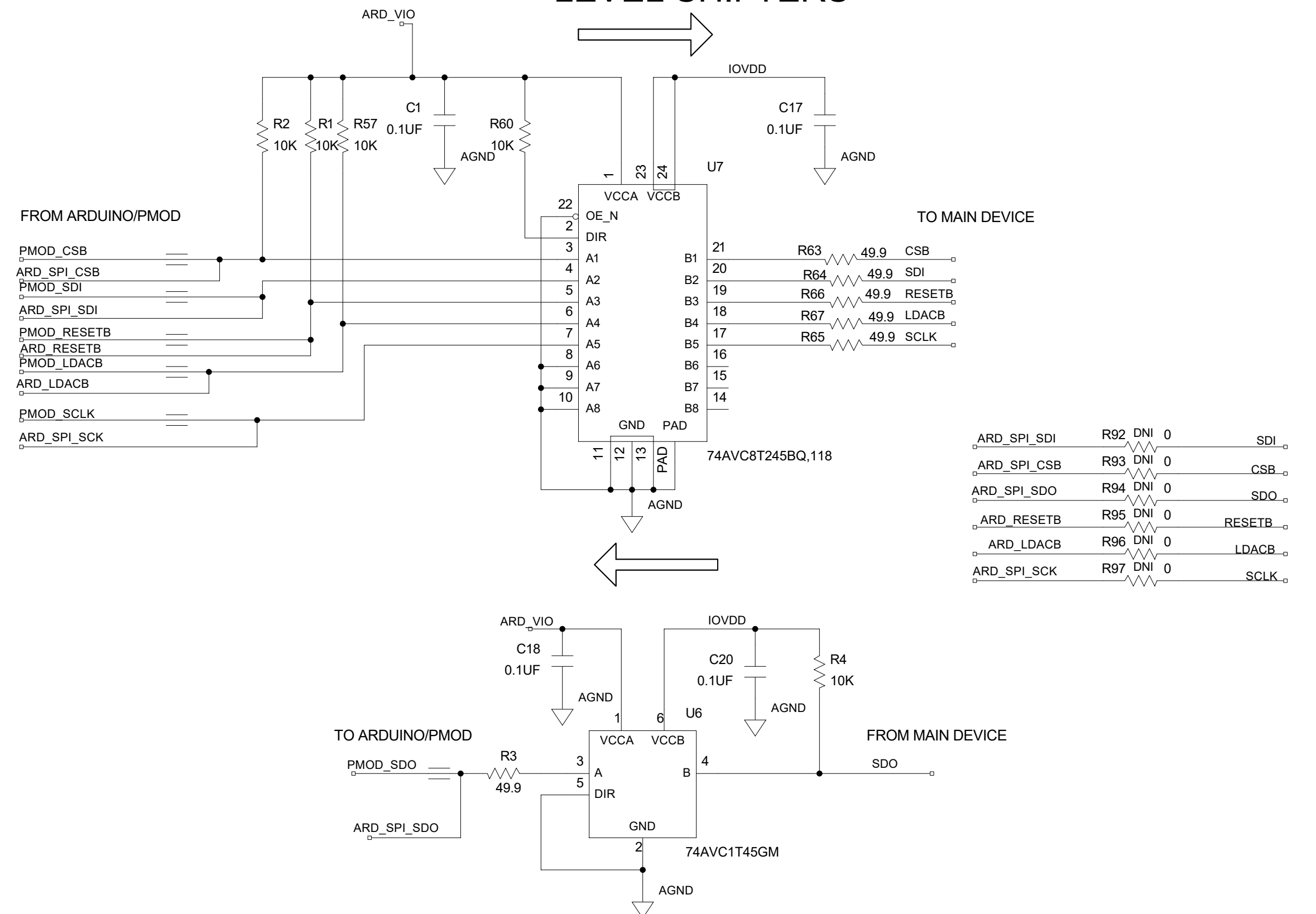
DIGITAL I/F HEADERS



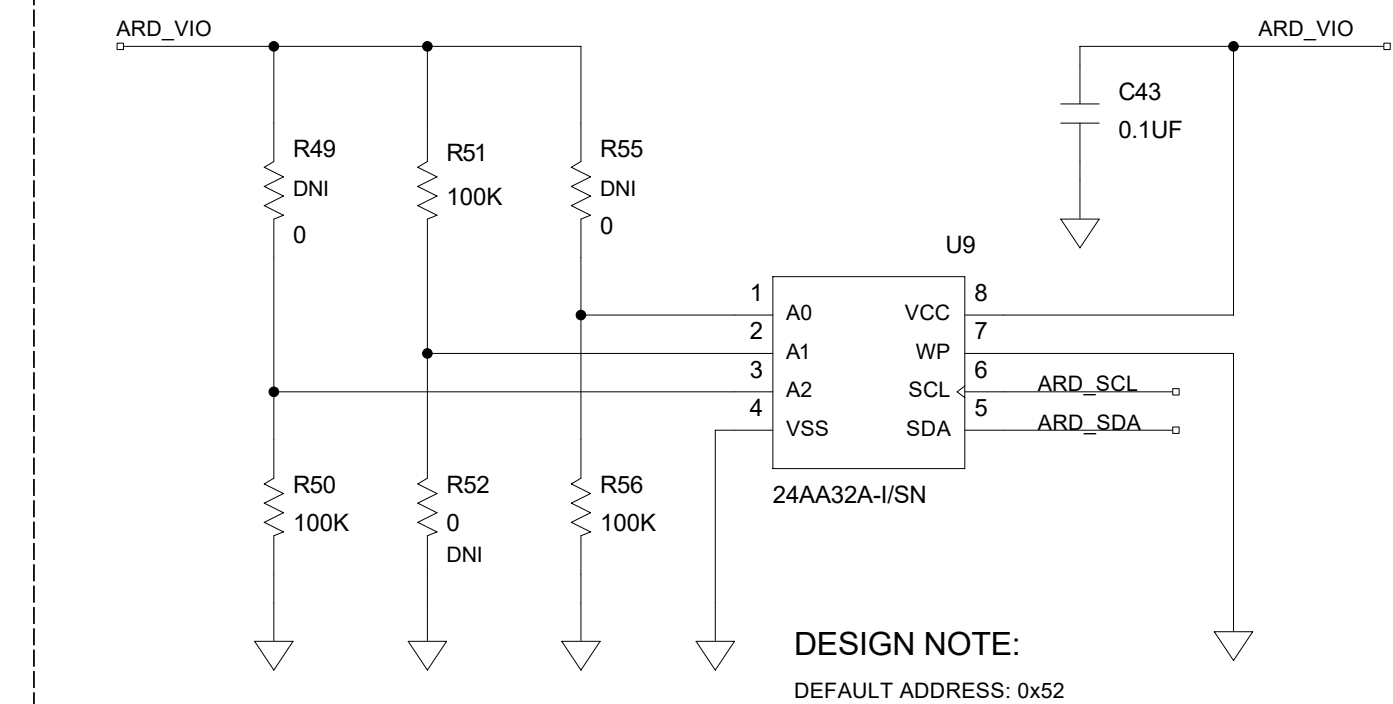
Reset Button



LEVEL SHIFTERS



EEPROM - BOARD ID



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SCHEMATIC			
HW TYPE : Customer Evaluation Z Product(s): AD5710R : AD5711R			
DESIGN VIEW \$DESIGN_VIEW	DRAWING NO. 02_083459		REV b1
PTD ENGINEER V. SETHIA	SIZE D	SCALE 1:1	SHEET 4 OF 4